

Electronic Acknowledgement Receipt

EFS ID:	7074305
Application Number:	09912721
International Application Number:	
Confirmation Number:	5513
Title of Invention:	DISTRIBUTED MICRO INSTRUCTION SET PROCESSOR ARCHITECTURE FOR HIGH-EFFICIENCY SIGNAL PROCESSING
First Named Inventor/Applicant Name:	Song Chen
Customer Number:	38881
Filer:	Laura C. Brutman/Kim Bonsignore
Filer Authorized By:	Laura C. Brutman
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Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for PTA recalculation in view of Wyeth	I4303_0116_ReqPTA.pdf	73012 a795c0b6ba4f821b3ae071278c24c36a6a1d5d7	no	1

Warnings:

Information:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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